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CRYSTAL OSCILLATOR Low Profile / High stability SPXO

SG-150S*E

•Frequency range : 3.000 MHz to 54.000 MHz •Supply voltage : 1.8 V / 2.5 V / 3.3 V

•Current consumption : 1.2 mA Typ.

(SEE: 1.8 V No load condition 40 MHz)

 $\begin{array}{lll} \bullet \text{Function} & : & \text{Standby}(\,\overline{\,\text{ST}}\,) \\ \bullet \text{External dimensions} & : & 2.1 \times 1.7 \times 0.75 \text{ mm} \end{array}$



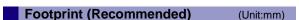
Specifications (characteristics)

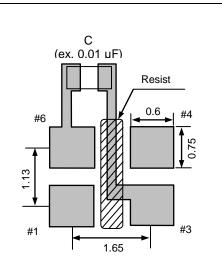
Item	Cymphol	Specifications			Conditions / Domester
	Symbol	SG-150SEE	SG-150SDE	SG-150SCE	Conditions / Remarks
Output frequency range	fo	3.000 MHz to 54.000 MHz			Please contact us for inquiries regarding the available frequencies.
Supply voltage	Vcc	1.8 V Typ. 1.6 V to 2.2 V	2.5 V Typ. 2.2 V to 2.7 V	3.3 V Typ. 2.7 V to 3.6 V	
Storage temperature	T_stg	-40 °C to +85 °C			Store as bare product.
Operating temperature	T_use	-40 °C to +85 °C			
Frequency tolerance *	f_tol	D: ±20 × 10 ⁻⁶ , E: ±15 × 10 ⁻⁶			-20 °C to +70 °C Vcc ±10%
	1_101	H: ±20 × 10 ⁻⁶ , T: ±15 × 10 ⁻⁶			-40 °C to +85 °C
Current consumption		2.3 mA Max.	2.5 mA Max.	3.5 mA Max.	No load condition,3 MHz≤fo≤32 MHz
	loo	2.8 mA Max.	3.0 mA Max.	4.0 mA Max.	No load condition,32 MHz <fo≤40 mhz<="" td=""></fo≤40>
	lcc	3.3 mA Max.	3.5 mA Max.	4.5 mA Max.	No load condition,40 MHz <fo≤48 mhz<="" td=""></fo≤48>
		4.5 mA Max.	5.0 mA Max.	6.0 mA Max.	No load condition,48 MHz <fo≤54 mhz<="" td=""></fo≤54>
Stand-by current	I_std	5.0 μA Max.			ST =GND
Symmetry	SYM	45 % to 55 %			50 % Vcc level,L_CMOS ≤ 15 pF
Output voltage	Voн	90 % Vcc Min.			IOH=-4 mA
	Vol	10 % Vcc Max.			IoL= 4 mA
Output load condition (CMOS)	L_CMOS	15 pF Max.			
Input voltage	VIH	80 % Vcc Min.			ST terminal
	VIL	20 % Vcc Max.			
Rise time / Fall time	tr/ tf		4.5 ns Max.	20 % Vcc to 80 % Vcc level, L_CMOS=15 pF	
Start-up time	t_str	5 ms Max.			t=0 at 90 % Vcc
Frequency aging	f_aging	This is included in frequency tolerance specification.			+25 °C, First year, Vcc= 1.8 V, 2.5 V, 3.3 V

^{*} Please contact us for inquiries regarding available frequency tolerance.

External dimensions (Unit:mm) 2.1 ± 0.1 0.405 #6 #5 #4 #6 26ABCD 1.7±0.1 C0.15 6789A 0.25 Pin map Pin Connection 2 (TEST) 3 GND 4 OUT 5 (TEST) 6 Vcc $\overline{\underline{ST}}$ pin = HIGH : Specified frequency output. \overline{ST} pin = LOW : Output is high impedance, oscillation stops. TEST pin: Do not connect externally.

(Recommend the design without the footprint.)





To maintain stable operation, provide a 0.01uF to 0.1uF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between Vcc - GND).